Attorney Docket No.: YOR9-2001-0512US1 (728-218)

REMARKS

Claims 1, 2, and 4-10 are pending in the application. All claims stand rejected.

The Examiner objected to claims 1, 2, 4, 5, and 7-10 because of informalities. In response, these informalities have been addressed and Claim 6 has been cancelled without prejudice. The Examiner rejected Claims 1, 2, and 4-10 under 35 U.S.C. §102(b) as allegedly being anticipated by U.S. Patent No. 5,528,177 ("Sridhar").

In response to the 35 U.S.C. §102(b) Claims 1, 2, 4-5, and 7-10 have been amended and Claim 6 has been cancelled. Claims 1, 4-5, 7-8, and 10 have been amended to claim an OR operation between a signal A and a signal B', as described in the inventive application in connection with the diagram in Figure 2C and the table in Figure 2D.

Sridhar does not teach or describe a circuit that can perform an OR operation between signals A and B' as recited in amended Claims 1, 2, 4, 5, and 7-10.

Below is a comparison of the output table of the circuits of Figure 2g of Sridhar and the inventive circuit of Fig. 2C of the present application.

Circuit of Sridhar Fig. 2g.

A	В	В'	231	232	131	132		
0	0	1	0	1	1	0		
0	1	0	1	0	0	1		
1	0	1	1	0	0	1		
1	1	0	1	0	0	1		

The output of the inventive circuit illustrated in Fig. 2C.

<u>A</u>	В	B'	A+B'
0	0	1	1
0	1	0	Ó
1	0	1	1
1	1	0	1

As can be clearly seen the output of the inventive circuit is clearly distinct and is not described by Sridhar.

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For at least the reasons discussed herein, Sridhar does not disclose, teach, or suggest the MOSFET logic circuit for performing OR operation recited in Claims 1, 2, 4, 5, and 7-10 as amended. It is therefore respectfully submitted that these claims are patentable over Sridhar. In view of the foregoing amendments and remarks, it is respectfully submitted that this application, and all pending claims, are in condition for allowance. Such early and favorable action is earnestly solicited.

Should the Examiner have any questions concerning this communication or feel that an interview would be helpful, the Examiner is requested to call the undersigned at the number indicated below.

Respectfully submitted,

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